National Journals/ International Journals 2020-21

- [1]. Naresh K. Darimireddy, Mohan Rao, U.; Park, Chan-Wang; Fofana, I.; Sujatha, M.; Verma, Anant K., "Perspectives of Convertors and Communication Aspects in Automated Vehicles, Part 2: Printed Antennas and Sensors for Automotive Radars" *MDPI Energies*, vol. 14, no. 6, p.1656, Mar. 2021. (Indexed in SCI-Expanded, Impact factor 2.7).
- [2]. Naresh K. Darimireddy, R Ramana Reddy, A.M Prasad, Wide-band Circularly-Polarized Cylindrical Dielectric Resonator Antennas with Rectangular Curved Slots. IEEE Antennas and Propagation Magazine, vol. 62, no.4, Dec. 2020 (Indexed in SCI-Expanded, Impact factor 3.765).
- [3].K. Lalith Kishore, R. Ramana Reddy, and **Naresh K. Darimireddy** Swastik Slotted Hexagonal Patch Antenna With Metamaterialbased Complementary Split-Ring Resonator Ictact Journal On Microelectronics, October 2020, Volume: 06, Issue: 03 Doi:10.21917/Ijme.2020.0173 (UGC)
- [4]. **Kiranmai Babburu, Kapila Gurucharan, S S Kiran, P Srujana**, PAPR Reduction Techniques And Image Quality Assessmentin Image Based MMS VLC System, IT in Industry, Vol. 9, N.2, 2021 (NJ)
- [5]. **Kiranmai Babburu, K Gurucharan, S S Kiran** A Brief Insight on Game Theoretic Approach for Wireless Communications, Solid State Technology Volume: 63 Issue: 5 Publication Year: oct, 2020. (International journal)
- [6].Kiranmai, Babburu & Kiran, S S & Babu, Mallavarapu & Gurucharan, Kapila & Thalluri, Lakshmi & Kumar, K. (2020). Automation of Smart Buildings and Integrated Message to Voice Voice Technology using Raspberry Pi 3 for IoT Applications. International Journal of Emerging Trends in Engineering Research. 8. 7425-7431. 10.30534/ijeter/2020/1228102020.
- [7]. **Kapila Gurucharan, S S Kiran, Kiranmai Babburu** Computer Vision Based Fruit Recognition And Classification System, International Journal of Future Generation Communication and Networking (IJFGCN), ISSN: 2233-7857(Print); 2207-9645(Online), NADIA, (2020), Vol. 13, No. 3, pp. 1-14.(Web of science)
- [8]. Allam, V., Madhav, B.T.P. & Anilkumar, T. A low-profile internet of things-controlled frequency reconfigurable triple band antenna for microwave sensing applications. Analog Integr Circ Sig Process (2021).
- [9]. Nancharaiah Vejendla, High Speed Split Radix FFT Processor Using Wallace Tree Multiplier, Solid State Technology Volume: 63 Issue: 5 Publication Year: 2020
- [10]. **D. Venkatachari,** Design of high gain dual T-shaped stub antenna for satellite communication TELKOMNIKA Telecommunication, Computing, Electronics and Control, Vol. 18, No. 3, June 2020, pp. 1209~1215 ISSN: 1693-6930
- [11]. **J. Priyanka**, K.Gayatri Design of Single Error Correction Codes with Fast Decoding for memories, The International journal of analytical and experimental modal analysis Volume XII, Issue VII, July/2020 ISSN NO:0886-9367
- [12]. **P. Hareesh**, Profound Learning of Facial Depth Maps for Obstructive Sleep Apnea Prediction, Journal of Research in Image and Signal Processing Volume 5 Issue 2 june 2020
- [13]. **P.Hareesh**, Ch.Geeth Nandini, A.Pavanasree, B.Priyanka, Ch.Mohankrishna," Early Detection and Classification of Infected Plant Leaves via various Diseases using SVM Technique", International Journal of Research in Electrical, Electronics and Communication Engineering, Volume 6 Issue 2

- [14]. **P. Hareesh**," Fast Alert Information of Earthquakes or Volcanoes in High Speed Railway using Wireless Data Network" International Journal of Research in Electrical, Electronics and Communication Engineering Volume 6 Issue 1
- [15]. Appalabathula Venkatesh, Shankar Nalinakshan,, **S S Kiran**, Pradeepa H,"Energy transmission control for a Grid- connected modern power system Non-Linear loads with a Series Multi-Stage Transformer Voltage Reinjection with controlled converters" 2020 International Journal of Engineering Trends and Technology 68(8):97-101
- [16]. Appalabathula Venkatesh, Shankar Nalinakshan, V. N. Jayasankar, V. Aneesh, S. S. Kiran & V. Sivasubramanian (2021) Stability Testing and Restoration of a DEIG-Based Wind Power Plant with Indirect Grid Control Strategies, IETE Journal of Research, DOI: 10.1080/03772063.2021.1934126
- [17]. **D.Venkatachari, B.RamaDevi**, High Gain Switched Beam Yagi-Uda Antenna for Millimeter Wave Communications, International Journal of Pervasive Computing and Communications (web of science)
- [18]. **A.Phani Madhuri, P.Srujana**, A Review On Coverage Analysis for Millimeter Wave Cellular Networks with Imperfect Beam Alignment, The International journal of analytical and experimental modal analysisnVolume XIII, Issue II, February/2021, ISSN NO:0886-9367
- [19]. **B. Sridhar, S. Sridhar , V. Nanchariah**, "Performance Evolution of Massive MIMO-OFDM for VLC Communications", i-manager's Journal on Communication Engineering and Systems, Vol. 9, No. 1, June 2020
- [20]. Damping Analysis To Improve The Performance Of Shunt Capacitive Rf Mems Switch Lakshmi Narayana Thalluri, K V V Kumar, Konari Raja Sekhar, N Bhushana Babu D, **S S Kiran**, Koushik Guha FACTA UNIVERSITATIS Series: Electronics and Energetics, https://doi.org/10.2298/FUEE2103381T September 2021 pp. 381-392 Vol. 34, No 3,

Book Chapters 2020-21

- 1. **Kiranmai B.**, LavanyaV., **Kiran S.S**. An Energy-Efficient Resource Allocation System Using OFDM DAS Model for LTE Applications. Communication Software and Networks. Lecture Notes in Networks and Systems, vol 134. Springer, Singapore.
- 2. **Ramamohan B.**, Usha S., Ananth P., Lalitha V.S., Jaswanth S. (2021) Design of Dumbbell-Shaped MIMO Antenna for Wearable Applications. In: Sekhar G.C., Behera H.S., Nayak J., Naik B., Pelusi D. (eds) Intelligent Computing in Control and Communication. Lecture Notes in Electrical Engineering, vol 702. Springer, Singapore. https://doi.org/10.1007/978-981-15-8439-8_24
- 3. Lakshmi Narayana Thalluri, K Srinivasa Rao, G Venkata Hari Prasad, **S S Kiran** (2021) Reconfigurable Antennas for RFID/GPS/WiMAX/WLAN Applications Using RF MEMS Switches. In: Biswas A., Saxena R., De D. (eds) Microelectronics, Circuits and Systems. Lecture Notes in Electrical Engineering, vol 755. Springer, Singapore. https://doi.org/10.1007/978-981-16-1570-2_21
- 4. S.B.T. Abyuday, R.Ramana Reddy, **Naresh K.Darimireddy** Millimeter wave wideband Koch fractal antennas. Springer lecture notes in electrical engineering book series.
- 5. L. N. Thalluri, K V V Kumar, K R Sekhar, N Bhushana Babu D, **S S Kiran**, Koushik Guha, "Iterative Approach and Structure Damping Analysis to Advance the Performance of Shunt Capacitive RF MEMS Switch" In Proceedings of the International Conference on Micr/Nano electronics devices, Circuits and Systems (MNDCS-2021), 30-31 January, 2021, India.

National Journals/ International Journals 2019-20

- [1]. **Naresh Kumar Darimireddy**, R Ramana Reddy, A.M Prasad "Wide-band Circularly-Polarized Cylindrical Dielectric Resonator Antennas with Rectangular Curved Slots" in *IEEE Antennas and Propagation Magazine*, August 2019. (Accepted) (Indexed in SCI-Expanded, Impact factor 3.804).
- [2]. V. Nancharaiah, N. Balaji, R. Ramana Reddy, Low voltage full swing Fin FET hybrid full adder", *International Journal of Recent Technology and Engineering (IJRTE)*, vol. 8, no. 2, pp. 4253-4263, July 2019. (Scopus Indexed)
- [3].**B. Kiranmai, S. S. Kiran, K. Guru Charan, M. Ravindra Kumar,** "Xilinx Based Electronic Voting Machine paper International Journal of Engineering and Advanced Technology, ISSN: 2249 8958, vol. 9 no.1, Oct. 2019 (Scopus Indexed)
- [4].**S S Kiran, Dr.B.Kiranmai, Dr.M.Rajan Babu**, Implementation of Covid-19 SARS Detection Method (CSDM) and Extended Simplified SIR Model (ESSM) International Journal of Advanced Science and Technology Vol. 29, No. 6, (2020), pp. 5975 5992
- [5]. M. Rajan Babu, Design Of Three-Layered Broadband Stacked Microstrip Patch Antenna, International Research Journal of Modernization in Engineering Technology and Science Volume 02 Issue:11, November -2020, Impact Factor- 5.354
- [6].S.Sridhar, S.Ramakrishna, Realization Of Array Multiplier And Serial-Parallel Multiplier Architectures, I managers journal on circuits and systems vol 7 no 3 junaug 2019
- [7]. **Siripurapu Sridar,** 130 Nm Cmos Technology Based Baugh-Wooley And Wallace-Tree-Multiplier Architectures, I managers journal on circuits and systems vol 7 no 3 jun-aug 2019
- [8]. **Dr. S. Sridhar**, Efficient High Speed Computing Low Power Multiplier Architecture using Vedic Mathematics For Digital Signal Processing Applications, International journal of engineering research and technology, 2019
- [9].**S. Sridhar**, VLSI Implementation of Linear Feedback Shift Register (LFSR) based Test Pattern Generator for Pseudo Exhaustive Testing, International Journal of Innovative Technology and Exploring Engineering (IJITEE) ISSN: 2278-3075, Volume-9 Issue-7, May 2020
- [10]. **B.sridhar**, **S.Sridhar V.Nanchariah**, MR Image Analysis of Neonatal Brain using Adaptive Mathematical Morphological Processing and Region based Segmentation, International Journal of Innovative Technology and Exploring Engineering (IJITEE) ISSN: 2278-3075, Volum,e-9 Issue-1, November 2019
- [11]. **Nancharaiah, B.Sridhar, S.Sridhar**, Design of Reversible Gates-Based Image Steganography using Quantum Dot Cellular Automata for secure Nano-Communications, International Journal of Recent Technology and Engineering (IJRTE) ISSN: 2277-3878, Volume-8 Issue-4, November 2019
- [12]. **B.Sridhar**, Development of a morphological image analysis based quality evaluation system for fruits and vegetables, I managers journal on image processing vol 6 no 4 oct-dec 2019
- [13]. **B.sridhar**, Implementation of image analysis system by using convolution neural networks, I managers journal on image processing vol 6 no 4 oct-dec 2019

- [14]. **B.Kiranmai**, Design and Implementation of a low latency Radix-8 FFT using FPGA Architecture, Juni Khyat ISSN: 2278-4632 (UGC Care Group I Listed Journal) Vol-10 Issue-5 No. 5 May 2020
- [15]. **P Srujana, S.S Kiran, B. Kiranmai** Developing A Idwt-Dwt Ofdm With Awgn Channel Using Differnt Modulation Techniques, Juni Khyat ISSN: 2278-4632 (UGC Care Group I Listed Journal) Vol-10 Issue-5 No. 5 May 2020
- [16]. **Dr B Kirnmai**, Real Time Implementation of Home Automation and Health Monitoring System using Google Assistant, International Journal of Advanced Science and Technology Vol. 29, No. 7s, (2020), pp. 2354-2361
- [17]. **K. Gurucharan, B. Kiranmai**, Xilinx Based Electronic Voting Machine, International Journal of Engineering and Advanced Technology (IJEAT) ISSN: 2249 8958, Volume-9 Issue-1, October 2019
- [18]. V. Narendra Reddy, V. Lavanya, B. Kiranmai, Radar Pulse Compression Waveform Generation Using FPGA Implementation, i-manager's Journal on Digital Signal Processing, Vol. 7 lNo. 2 lApril June 2019
- [19]. **B. Sridhar**, Development of IoT Based Smart Dustbin Monitoring System, International Journal of Engineering and Advanced Technology (IJEAT) ISSN: 2249 8958, Volume-9 Issue-5, June 2020
- [20]. **B. Sridhar**, Development of Raspberry-Pi Based IoT Agricultural System using Fuzzy Techniques, International Journal of Soft Computing and Engineering (IJSCE) ISSN: 2231-2307, Volume-10, Issue-1, July 2020
- [21]. **B. Kiranmai**, Implementation of an optimized DFT- Spread-OFDM technique to minimize PAPR Using QPSK Symbols in 5G technology, Mukt Shabd Journal Volume IX Issue VI, JUNE/2020 Issn No : 2347-3150
- [22]. **Babburu Kiranmai**, Bylapudi Rama, Developing Spatial Modulation Technique For The Improvement Of Ber And Spectral Efficiency Over Mrc And Mimo In 5g Technology, Alochana Chakra Journal Volume IX, Issue VII, July/2020 ISSN NO:2231-3990
- [23]. **A.V. Paramkusam**, Development of a New Blind Watermarking for Medical Images, International Journal of Innovative Technology and Exploring Engineering (IJITEE) ISSN: 2278-3075, Volume-9 Issue-9, July 2020
- [24]. **A.V.Paramkusam**, An Effective Sharp Curve Lane Detector, International Journal of Innovative Technology and Exploring Engineering (IJITEE) ISSN: 2278-3075, Volume-9 Issue-9, July 2020
- [25]. **Ch. Gayatri**, An Approach For Dehazing Of Underwater Image By Using Multi-Scale Fusion Strategy Juni Khyat ISSN: 2278-4632 (UGC Care Group I Listed Journal) Vol-10 Issue-6 No. 1 June 2020.
- [26]. **Kowju.Gayatri**, V. JyoshnaMouni,,P. Bhanu, P.A. Bhaskara Krishna An Efficient Brain Tumor cells Detection and categorizing using SVM. Advanced Science Letters ISSN:1936-6612 june 2020 (IJ)
- [27]. **S S Kiran**, Implementation of Massive MIMO Systems for 512-Point FFT Processor using VLSI Technology, Mukt Shabd Journal, Volume IX, Issue VI, JUNE/2020 ISSN NO: 2347-3150
- [28]. **V.Nancharaiah,** Design of Multilayer ALU Using Landauer Clocking in Field coupled Nano Technology, Juni Khyat ISSN: 2278-4632 (UGC Care Group I Listed Journal) Vol-10 Issue-5 No. 7 May 2020
- [29]. **V. Nanchariah** B. Sridhar Indira. S, FPGA Implementation Radix-2 Architecture For Twin Data, i-manager's Journal on Circuits and Systems, Vol. 7 · No. 4 · September November 2019

- [30]. **B. Rama Mohan**, Design of Triband Truncated Patch and Circular Patch Antenna, Trends in Microwave Engineering and Technology Volume 5 Issue 1 (NJ)
- [31]. Ch. Gayatri, An Implementation Of Deblurring Of An Image Using Hierarchical Approach, Mukt Shabd Journal ISSN NO: 2347-3150.
- [32]. **D. Suresh Kumar** Implementation Of Power Reduction In Cmos Circuits, i-manager's Journal on Circuits and Systems, Vol. 7 · No. 4 · September November 2019
- [33]. **D. Suresh Kumar**, Design Of Low Power Full Adder Using Adiabatic Logic, Mukt Shabd Journal ISSN No: 2347-3150 Volume Ix Issue Iv, April 2020
- [34]. B. Hemanth Nag, Design Of Smart Drip Irrigation System For Farming International Journal of Scientific Research in Engineering and Management (IJSREM) Volume: 04 Issue: 05 | May -2020
- [35]. **Phanimadhuri, J. Priyanka** An Improved Design Of Approximate Arithmetic Units Using Reversible Logic Gates Journal of Emerging Technologies and Innovative Research JETIR June 2019, Volume 6, Issue 6
- [36]. **J. Priyanka,** Design of Parity Generator Circuits using QCA, Journal of Power Electronics and Drives Volume 5 Issue 1
- [37]. **M. Ravindra Kumar**, Implementation of 32-Bit Carry Look Ahead Adder Using GDI Design, International Journal of VLSI Design, Microelectronics and Embedded System Volume 5 Issue 1
- [38]. **P. Hareesh**, Plan and Investigation of Digital Filters for Discourse Signals Utilizing MDSP, Journal of Research in Image and Signal Processing Volume 5 Issue 2
- [39]. **P. Srujana**, Designing Of IFFT-FFT OFDM Using Different Digital Modulation Techniques International Research Journal of Engineering and Technology (IRJET) e-ISSN: 2395-0056 Volume: 07 Issue: 05 | May 2020 P-ISSN: 2395-0072
- [40]. **P.Srujana1, J.Priyanka, A.Phani Madhuri**, Implementing Morphology on Images And Its Applications Using Gui, International Journal of Engineering Trends and Technology, Special issue on Trending Technology 2020 ISSN: 2231-5381
- [41]. **R.V.CH. Sekhar Rao,** Implementation of Distance Vector and Differential Evolution Node Localization Algorithms by Received Signal Strength Indicator, Trends in Microwave Engineering and Technology Volume 5 Issue 1, 2020
- [42]. **R.V.CH.sekhar rao**, Indoor Localisation Accuracy Improvement In Wireless Sensor Networks Using Convolutional Neural Network, Juni Khyat ISSN: 2278-4632 (UGC Care Group I Listed Journal) Vol-10 Issue-5 No. 14 May 2020
- [43]. **R. Divya Kanti**, Method of Visual and Infrared Image Fusion for Object Detection, Journal of Research in Image and Signal Processing Volume 5 Issue 1.2020.
- [44]. **N. Raja Sekhar**, Design Of Hybrid Dielectric Resonator Antenna For Multifunctional Applications, i-manager's Journal on Electronics Engineering, Vol. 10l No. 3l March May 2020
- [45]. Kavya., Sri Sowmya, Lavanya, Sree Jhar, **N..Raja Sekhar**, Design of MIMO Dielectric Resonator Antenna with Improved Isolation for C-Band Applications, Trends in Microwave Engineering and Technology Volume 5 Issue 2, 2020.
- [46]. **N. Rajasekhar**, Investigation on Reconfigurable Inverted F-Shaped Monopole Antenna for Dual Band Applications, Journal of Electronics and Optical Communication Engineering Volume 5 Issue 1, 2020.
- [47]. **Sunkana Rama Krishna**, An Efficient VLSI Design of 32X32 bit Multiplier using Wallace Tree Algorithm in Vivado HLS and Xilinx ISE Software using VHDL,

- International Journal of Innovative Technology and Exploring Engineering (IJITEE) ISSN: 2278-3075, Volume-9 Issue-7, May 2020
- [48]. **K. Gayathri,** Nagasai Mounika, K. Sasi Swetha Sruthi M. Santosh G. Srinu S. Rama Krishna Implementation Of Faster Region-CNN For Image Classification Using MATLAB International Journal of Electronics and Communication Engineering and Technology (IJECET) Volume 11, Issue 1, January-April 2020, pp. 10-17.
- [49]. **S. Suresh Kumar,** IOT Based Smart Sensor Home, Journal of Research in Instrumentation and Control Engineering Volume 5 Issue 1,2020.
- [50]. **Mr.Shaik Azeez,** Recognization Of Denoising Qr Code Image Using Morphological Operations, International Journal of Engineering and Applied Sciences (IJEAS) ISSN: 2394-3661, Volume-4, Issue-3, March 2020
- [51]. **V.Y.S.S. Sudir Patnaikuni**, Accident Identification with Automatic Ambulance Rescue System, Journal of Advances in Communication Engineering and Its Innovations Volume 5 Issue 1.
- [52]. **Mandala Sujatha**, Implementation Of Ultra-Wide Band Microstrip-To-Microstrip Vertical Transition, i-manager's Journal on Electronics Engineering, Vol. 10 lNo. 3lMarch May 2020
- [53]. **M. Sujatha**, K. Tharangini, K. S. P. Madhavi, K. E. V. S. S. Murthy, M. Jagadeesh Some Investigations on Wearable Antennas for Wireless Applications, Trends in Microwave Engineering and Technology Volume 5 Issue 2 ,2020.
- [54]. **B. Hemanth Nag**, Vadigalla Nagamani, Tharun Kumar Amarapalli, Rampalli Sandhya, Velduti Venu Venkatesh, "Development Of Real Time Text Recognition System For Visuallay Impaired People", International Research Journal of Engineering and Technology (IRJET) e-ISSN: 2395-0056 Volume: 07 Issue: 06 | June 2020 www.irjet.net p-ISSN: 2395-0072

NATIONAL/INTERNATIONAL JOURNALS 2018-19

- [1]. **B. Sridhar**, "FPGA Implementation of 64/128/256 Point Radix 8 Pipelined FFT/ IFFT Core", *International Journal of Management, Technology and Engineering*, vol. 9 & no.1, 2249-7455, Jan 2019.
- [2]. **B. Sridhar**, "Railway Track fault monitoring system using signal processing techniques", *Journal of Digital signal processing*, vol. 6, no. 3, 2018.
- [3]. **S. Sridhar**, "Quantum state based to Image representation", *IEEE-ICCMC* (Communicated), 2018,
- [4]. **Naresh Kumar Darimireddy**, Asymmetric and Symmetric modified bow-tie slotted circular patch antennas for circular polarization, *Wiley-(Electronics & Telecommunication Research Institute (ETRI) Journal*, Vol. 40, no. 5, Aug. 2018, SCI and IF: 1.306.
- [5]. **Naresh Kumar Darimireddy**, Tri-band and quad-band dual L- slot coupled circularly polarized dielectric resonator antennas, *Wiley-International Journal RF and Microwave Computer aided Design*, vol. 28, no. 8, Oct. 2018, SCI and IF: 1.116.
- [6]. **Naresh Kumar Darimireddy**, "Circular Conformal Antenna Array With Enhanced Gain for Airborne Applications," *i'manager-Journal of Communication Engineering and Systems*, vol. 7 & no. 4, Oct. 2018, 2277-5102, Indian Citation Indexed (ICI), Free

Journal.

- [7]. **Naresh Kumar Darimireddy**, "A MIMO Shark-Fin Antenna for Vehicular Communication Applications", *i' manager-Journal of Communication Engineering and Systems*, vol.7, no. 4, Oct. 2018, 2277-5102, Indian Citation Indexed (ICI), Free Journal.
- [8]. Naresh Kumar Darimireddy, "Design of High Speed and Low Power Multiplier using Dual-Mode Square Adder", *Int. J. Circuits & Architecture Design, (Accepted and Awaiting Production)*, April 2019, 2051-7025, Indian Citation Indexed (ICI), Free Journal.
- [9]. **B. Kiranmai**, "Developing Sidelobe Reduction Techniques using P4 Code for Pulse Compression Radar Applications", *i-manager's Journal on Digital Signal Processing*, vol. 7, no. 1, March-2019, 2322-0368, Indian Citation Indexed (ICI), Free Journal.
- [10]. **V. Nancharaiah**, "FPGA Implementation of 64/128/256 Point Radix 8 Pipelined FFT/ IFFT Core", *International Journal of Management, Technology and Engineering*, vol. 9, no. 1, Jan 2019, 2249-7455.
- [11]. **V. Nancharaiah**, "Implementation of Low-Power Split-Radix FFT Processor", *International Journal of Management, Technology and Engineering*, vol. 8, no. 12, Dec 2018, 2249-7455.
- [12]. **B. Ramamohan**, "Implementation of Low-Power Split-Radix FFT Processor", *International Journal of Management, Technology and Engineering*, vol. 8, no. 12, Dec 2018, 2249-7455.
- [13]. **V.Y.S.S Sudir Patnaikuni**, "Implementation of Low-Power Split-Radix FFT Processor", *International Journal of Management, Technology and Engineering*, vol. 8, no. 12, Dec 2018, 2249-7455.
- [14]. **J. Priyanka**, "Performance Comparison of FH_CDMA Scheme using Chaotic Sequences over Fading Channels", IOSR, vol.13, no. 5, Sep-Oct. 2018, 2278-2834.
- [15]. **K. Madhavi**, "Performance Comparison of FH_CDMA Scheme using Chaotic Sequences over Fading Channels", IOSR, vol.13, no. 5, Sep-Oct. 2018, 2278-2834.
- [16]. **P. Divya**, "Performance Comparison of FH_CDMA Scheme using Chaotic Sequences over Fading Channels", IOSR, vol.13, no. 5, Sep-Oct. 2018, 2278-2834.
- [17]. **B. Jayalakshmi**, "Design of High Speed and Low Power Multiplier using Dual-Mode Square Adder", *Int. J. Circuits & Architecture Design*, (Accepted and Awaiting Production), 2019, 2051-7025, Indian Citation Indexed (ICI), Free Journal.
- [18]. **D. Venkatachari**, Designing of optimized energy D Flip Flop using inverse narrow width with pull up/down network, IOSR, AE96078, 2018-19.

- [1]. **Naresh Kumar Darimireddy**, A miniaturized Hexagonal Triangular Fractal Antenna for wide-band applications, IEEE Antennas & Propagation Magazine, Vol. 60, No. 2, April 2018, SCI and IF: 3.007.
- [2]. **Naresh Kumar Darimireddy**, Asymmetric triangular semi- elliptic slotted patch antennas for wireless applications, Radio Engineering Journal, Vol. 27, No.1, April 2018, SCI and IF: 1.048.
- [3]. **M. Rajan babu**, "A Case Study on MAC Design using High-Performance Nikhilam-Sutra Vedic Multiplier", *Emperor International Journal Of Finance And Management Research*, Volume-III, Issue 11, p.no-144-149, November 2017 CHENNAI, 2395-5929 & I.F 1.4.
- [4]. **M. Rajan babu**, "A High-Performance and Low-Power Pipeline Vedic Multiplier using Adiabatic Logic, *Emperor International Journal Of Finance And Management Research*", Volume-III, Issue 11, p.no-150-156, November 2017 CHENNAI, 2395-5929 &I.F 1.4.
- [5]. **M. Rajan babu**, A Study on ALU design using Dynamic CMOS Logic Families, "*Emperor International Journal Of Finance And Management Research*", Volume-III, Issue 11,p.no-157-163, November 2017 CHENNAI, 2395-5929, & I.F 1.4.
- [6]. **M. Rajan babu**, "High-Performance 2-Way Pipeline Truncated Multiplier for DSP Applications", Emperor International Journal Of Finance And Management Research, Volume-III, Issue 11, p.no-164-170, Nov–2017, CHENNAI,2395-5929& I.F 1.4.
- [7]. **B. Sridhar**, Smart Image analysis based agri advisory system for rice crops, Journal of software Engineering, Vol. 12 & No. 3, 2018, 2230-7168.
- [8]. **B. Kiranmai**, "Sidelobe Level Suppression Using Amplitude Shift Code Technique For Polyphase Codes", *IPASJ INTERNATIONAL JOURNAL OF ELECTRONICS & COMMUNICATION (IIJEC)*, vol. 6, no. 2, February 2018,2321-5984.
- [9]. **V. Nancharaiah**, Implementation of Power Optimized FIP Filter Architecture, IJVDCS, Vol. 6, Page No. 406-410, Jan 2018, 2322-0929.
- [10]. **V. Nancharaiah**, Realization of Optimized Wallace Multiplier Based FFT Architecture, IJVDCS, Vol. 6, Page No. 401-405,Jan 2018,2322-0929.
- [11]. **V. Nancharaiah**, Design and Implementation of FFT using CMOS Cascage Current Mirror, IJVDCS, Vol. 6, Page No. 138-142, Jan 2018, 2322-0929.
- [12]. **V. Nancharaiah**, Design of Area Efficient 64-Bit MDC FFT Processor, IJVDCS,Vol. 6, Page No. 015-019,Jan 2018, 2322-0929.
- [13]. **Mr. V. Nancharaiah**, Performance Analysis of Adder Circuits using FINFETs, i-Managers Journal on Circuits and Systems, Vol. 5. No. 3, Aug 2017, Indian Citation Indexed (ICI), Free Journal.
- [14]. **S. Suresh Kumar**, Agri IOT soil Moisturizing System , JETIR, vol.no.5, Aug, 2018,2349-5162.
- [15]. **A. Pramod Kumar**, Modeling And Implementation Of A New Zcs Interleaved Bidirectional Buck-Boost DC-DC Converter For Energy Storage Systems, Electr Eng DOI-10.1007/S00202-017-0632-1,Volume 99, <u>Issue 4</u>, pp 1283–1293, December 2017, GERMANY, ISSN 0948-7921.
- [16]. **A. Pramod Kumar**, Design and simulation of a New ZVT Bi-directional DC-DC converter for electric vehicles, Indonesian Journal Electrical Engineering and

- Computers Science (Indonesia), VOL 7, NO-1, PP-75-83, JULY-2017, DOI-10.11591.
- [17]. **A. Pramod Kumar**, Comparative study of Soft-switching Non-isolated DC-DC Converters, Journal of Advanced Research in dynamical and Control Systems, Issue 18, Vol 92017, pp 1730-1742, 2017, 9(18):1730-1742.

NATIONAL/INTERNATIONAL JOURNALS

2016-17

- [1]. **M. Rajanbabu**, Implementation Of Efficient Johnson Counter Using Diode Free Adiabatic Logic, IOSR, Vol-12, NO-2, PP-24-32, Mar.-Apr. 2017, 2278-8735, 3.12.
- [2]. **Naresh Kumar Darimireddy**, Design of Triple-Band CPW Fed Circular Fractal Antenna, IJCI,Vol. 5, No. 4, pp. 1-7, August 2016, 2320 8430.
- [3]. **B. Sridhar**, Deformable Model Based Marked Controlled Liver Ct-Scan Image Segmentation, IJETT, Volume 46 Number 4, pp-226-232, April 2017, 2231-5381, I.F1.656.
- [4]. **B. Sridhar**, Multilevel Fuzzy Threshold Image Segmentation Method For Industrial Application, IOSR, Volume 12, Issue 2, Ver. III PP 06-17, Mar 2017, 2278-8735, I.F3.12.
- [5]. **S. Sridhar**, Text Data Hiding Within the Image by Using LSB Technique, IJESC, Volume 7 Issue No.3, Mar 2017, 2321- 3361, I.F3.168.
- [6]. **S. Sridhar**, Face Reorganization Using SOM based neural networks and PCA algorithm, IOSR, VOL-12, NO-2, PP-24-32, Mar.-Apr. 2017, 2278-8735, I.F3.12.
- [7]. **V. Nancharaiah**, Performance evaluation of full adder, IOSR, Volume 12, Issue 2, Ver. II PP 34-37, Mar.-Apr. 2017, 2278-8735, I.F3.12.
- [8]. **V. Nancharaiah**, CMOS Full Adder and Multiplexer Based Encoder for Low Resolution Flash ADC, IOSR, Volume 12, Issue 2, Ver. II PP 20-27, Mar.-Apr 2017, 2278-8735, I.F3.12.
- [9]. **V. Nancharaiah**, Energy efficient multiplier design using GDI logic, IOSR, Vol-12,No-2, PP-33-39, Mar.-Apr 2017, 2278-8735, I.F3.12.
- [10]. **R. V. Ch. Sekhar Rao**, Objective Quality Assessment of Contrast-Distorted Images Based on Natural Scene Statistics, IJESC, Vol-12, No-2, Pp5911-17, Mar.-Apr 2017, 2278-8735, I.F3.12.
- [11]. **R. V. Ch. Sekhar Rao**, Automatic Routing of Traffic Signaling using Image Processing, IJATIR, Vol.09, Issue.05, Pages:0670-0674, April-2017, 2348–2370, I.F3.168.
- [12]. **K.T.P.S Kumar**, Design Of Automatic Accident Intimation Device from black spot through SMS(GSM), ZIGBEE, IJESC,Vol-7, no-3, pp-6065-68, March 2017, 2321-3361, I.F3.168.
- [13]. **K.T.P.S Kumar**, Design of Triangular Patch Antenna with reduced RCS for Stealth Applications using Bionics, IJIRCCE, Vol. 5, Issue 3, March 2017, 2320-9798, I.F6.577.
- [14]. **B. Rama Mohan**, Detection of Drowsiness, IJESC, Volume 7 Issue No.3, March 2017,

- 2321-361, I.F3.168.
- [15]. **B. Rama Mohan**, Speaker Identification using MFC, IJESC, Volume 7 Issue No.4, March 2017, 2321-3361, I.F 3.168.
- [16]. K. Prasanna Kumar, Design and Analysis of Implanted Low Profile Antenna for Bio-Medical Applications, IJIRCCE, Vol. 5, Issue 3, March 2017, March 2017, 2320-9798, I.F6.577.
- [17]. **K. Prasanna Kumar**, Ultra-Wideband Printed Monopole Antenna for WLAN & Satellite Communication, IJIRCCE, Vol. 5, Issue 3, March 2017, March 2017, 2320-9798, I.F 6.577.
- [18]. **B. Hemanth Nag**, Identification of high performance full substractor using fin-FET, IJESC, Volume 7 Issue No.3, Mar 2017, 2321 3361, I.F3.168.
- [19]. **N. Raja sekhar**, Hybrid Z-Shaped cylindrical dielectric resonator antenna, IJESC, Vol-7, no-4, pp 6281-83, Mar 2017, 2321 3361, I.F 3.168.
- [20]. **N. Raja sekhar**, Multi slot UWB antennas to minimize the interferences from WLAN& x-band applications, IOSR, vol-12, no-2, pp-50-54, Mar 2017, 2278-8735, I.F 3.12.
- [21]. A High Speed Low Power Content Addressable Memory Design with A Parity Bit and Power Gated Ml Sensing using Match Line, IJESC, Volume 7 Issue No.4,Mar 2017,2321 3361, I.F 3.168.
- [22]. **Ch. Gayatri**, Image Compression using combined approach of EZW and LZW,IJERA,Vol-7,no-3,pp-82-87,Mar 2017,2248-9622.
- [23]. **Ch. Gayatri**, Comparative Approach For Image De-Noising Using ANN Based Detector And Bilateral Filter, IJESC, Vol-7, no-3, Mar2017, 2312-3361, I.F 3.168.
- [24]. **Sk. Azeez,** De-noising and de blurring or QR codes by using median filter, IJERAM, Vol. 03, Issue 03, PP. 68-71, Mar2017, 2456-2033.
- [25]. **Sk. Azeez**, Automatic Vehicle Number Detection Using MATLAB, IJEAS, Volume-4, Issue-3, Mar2017, 2394-3661, I.F 6.505.
- [26]. **Y. V. Koteswara Rao**, Hexagonal Shaped Micro-strip Patch Antenna for Wi-Fi Application, IJIRCCE, Vol. 5, Issue 3, March 2017, 2320-9798, I.F 6.577.
- [27]. **Ch. Suryanarayana**, Design Of High Speed Vedic Multiplier, IJISRT, Volume 2, Issue 3, March –2017, 2456- 2165, I.F 3.585.
- [28]. **Ch. Suryanarayana**, Design of Code Converters And Multiplier Using Reversible Logic Gates, IJISRT, Volume 2, Issue 4, April–2017, 2456-2165, I.F 3.585.
- [29]. **V. Y. S. S. Sudir Patnaikuni**, CMOS full adder and multiplexer based encoder for low resolution flash ADC, IOSR, Vol-12, no-2, pp-20-27,March –2017, 2278-8735, I.F 3.12.
- [30]. **S. Suresh Kumar**, Designing of Smart Home using Android Mobile, IJESC, Volume 7, Issue No.4, March 2017, 2321-3361, I.F 3.168.
- [31]. **J. Priyanka**, High Speed and Area Efficient Booth Multiplier Using SQRT CSLA with Zero Finding Logic, IJERA, Vol. 7, Issue 4, pp.75-80, March –2017, 2248-9622.
- [32]. **Md. Azima**, Design of High-Speed Dynamic Double-Tail Comparator, IJERD, Volume 13, Issue 4, PP.10-21, March 2017, 2278-800X.

- [33]. **V. Swetha**, Design and Analysis of Hybrid C-MOS 4-Bit Parallel Adder, IOSR, Volume 12, Issue 2, Ver. III, March 2017, 2278-8735, I.F 3.12.
- [34]. **V. Swetha**, High Performance and Low power VLSI CMOS Circuit Designs using ONOFIC Approach, IJERA, Vol. 7, Issue 3, pp.71-76, March 2017, 2248-9622.
- [35]. **T. Krishna Mohana,** Systematic Approach of BER Analysis in OFDM using BPSK, QPSK and Signal to Noise Ratio for MIMO-OFDM, IJESC, Volume 7 Issue No.4, PP-6655-58, March 2017, 2321-3361, I.F 3.168.
- [36]. **T. Krishna Mohana**, Various Distance Metric Methods For Query Based Image Retrieval, IJESC, Volume 7 Issue No.4, PP-6655-58, March 2017, 2321-3361, I.F 3.168.
- [37]. **T. Akshay Kumar**, Speaker Identification using MFC, IJESC, Volume 7 Issue No.4,PP-6655-58, March 2017, 2321-3361, I.F 3.168.
- [38]. **K. Madhavi**, High –Speed Implementation of Design and Analysis by Using Parallel Prefix Adders, IOSR-JECE, Volume 12, Issue 2, Ver. II,PP 44-49, March 2017, 2278-8735, I.F 3.12.
- [39]. **R. Lalitha**, Design of High-Speed Dynamic Double-Tail Comparator, IJERD, Volume 13, Issue 4, PP.10-21, March 2017, 2278-800X.
- [40]. **R. Lalitha**, High Speed and Area Efficient Booth Multiplier Using SQRT CSLA with Zero Finding Logic, IJERA, Vol. 7, Issue 4,pp.75-80, March –2017, 2248-9622.
- [41]. **P. Divya**, High –Speed Implementation of Design and Analysis by Using Parallel Prefix Adders, IOSR-JECE, Volume 12, Issue 2, Ver. II,PP 44-49, March 2017, 2278-8735, I.F 3.12.
- [42]. **P. Devi**, Design of Triangular Patch Antenna with reduced RCS for Stealth Applications using Bionics, IJIRCCE, Vol. 5, Issue 3, March 2017, 2320-9798, I.F 6.577.
- [43]. **P. Devi**, Design and Analysis of Implanted Low Profile Antenna for Bio-Medical Applications, IJIRCCE, Vol. 5, Issue 3, March 2017, 2320-9798, I.F 6.577.
- [44]. **P. Devi,** Hexagonal Shaped Micro-strip Patch Antenna for Wi-Fi Application, IJIRCCE, Vol. 5, Issue 3, March 2017, 2320-9798, I.F 6.577.
- [45]. **P. Devi,** Hybrid Z-Shaped cylindrical dielectric resonator antenna, IJESC, Vol-7,no-4,pp 6281-83, MAR 2017, 2321 3361, I.F 3.168.